



PATENT

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Jiang et al.

Serial No.: 10/706,576

Filed: November 12, 2003

For: METHODS FOR ASSEMBLING  
MULTIPLE SEMICONDUCTOR DEVICES

Confirmation No.: 6662

Examiner: J. Dolan

Group Art Unit: 2813


Attorney Docket No.: 2269-4886.1US  
(01-0201.01/US)

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AMENDMENT UNDER 37 C.F.R. §1.116

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Sir:

This Amendment follows the Final Office Action dated November 30, 2005, the three-month shortened statutory period for response to which expires on February 28, 2006. This Amendment is being submitted within two months of the mailing date of the Final Office Action.

An amendment to the specification appears on page 2 of this paper;

A listing of the claims, in which amendments to the claims are presented, begins on page 3 of this paper; and

Remarks start at page 8 of this paper.